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PATENT  
1248-0538P#4 / Letter  
10-15-01  
R. Stokes

## IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Eiji YANAGAWA et al.  
Application No.: 09/820,671 Group: Not Assigned  
Filed: March 30, 2001 Examiner: Not Assigned  
For: SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

LETTERAssistant Commissioner for Patents  
Washington, DC 20231

June 22, 2001

Sir:

Under the provisions of MPEP § 2001.06(b), the Examiner is hereby advised of the following co-pending U.S. Application(s):

<u>Application No.</u>	<u>Filing Date</u>	<u>Group</u>
09/551,741	April 18, 2000	2815
09/834,605	April 16, 2001	Unassigned

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The subject matter contained in the above-listed co-pending U.S. application(s) may be deemed to relate to the present application, and thus may be material to the prosecution of this instant application.

The above-listed co-pending application(s) is(are) not to be construed as prior art. By bringing the above-listed application(s) to the attention of the Examiner, Applicant(s) do(does) NOT waive any confidentiality concerning the above-listed co-pending application(s) or this application. See MPEP § 101. Furthermore, if said application(s)

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should not mature into patents, such application(s) should be preserved in secrecy under the provisions of 35 U.S.C. § 122 and 37 C.F.R. § 1.14.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By

  
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DJD:kna

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